



JULIET Series 2

Dynamic Mixed Signal Test and ISP
for Prototyping and Production



- finding short circuits and breaks
- checking external interfaces
- programming the assembly
- checking the application's functions
- calibrating the assembly



Boundary Scan Test

- IEEE 1149.1
- IEEE 1149.6
- IEEE 1149.8.1
- external I/O
- RAM test
- cluster test

Flash Programming

- Flashes (NAND, NOR)
- Bad Block Handling
- PLD/CPLD
- MCU – internal Flash
- Serial numbers
- Calibration data

JULIET Series2

Complete JTAG/
Boundary Scan Tester
in a desktop system
for prototyping and
production

Test Interface

- 4 power supplies (internal/external)
- 4 test access ports (TAP) up to 50MHz
- 192 test channels (digital/analogue)
- 16 electrical isolated I/O test channels
- 32 purely analogue test channels
- 8 interfaces for RS232
- 4 interfaces for RS485

Functional Test

- analogue drive and measure
- digital drive and measure
- transition detector
- waveform generator
- frequency measurement
- logic analyser

Mechanics

- compact, portable desktop mode
- solid mechanics for manual loading and unloading
- interchangeable cartridges for bed and nails
- contacting of connectors
- LED display of all test results
- operation via control panel during production
- front interface for measuring all signals

Software

- Tester Link Software
- online PASS/FAIL statistic
- SYSTEM CASCON™
- optional MES interface

ESA Technologies

- JTAG/Boundary Scan
- ChipVORX®
- VarioTAP®
- VarioCore

• Made in Germany